

Abstract

The invention relates to a pulse-generator circuit (201) for generating an input signal for a flip-

5 flop circuit from a clock-pulse signal (CLK) and a data signal (D). Said circuit comprises a

control unit for controlling a clock-pulse field effect transistor (205), a logic field effect

transistor (207) and a feedback field effect transistor (209). To generate the input signal, the

control unit is configured in such a way that the clock-pulse field effect transistor is controlled

chronologically after the logic field effect transistor and the feedback field effect transistor, thus

10 generating the flip-flop signal.